

ABSTRACT OF THE DISCLOSURE

A semiconductor memory device uses memory cells, which have structures not increasing areas, and are arranged in a distinctive manner providing high data holding stability.

5 A semiconductor memory device includes memory cells formed on a main surface of a semiconductor substrate, and each having first and second transistors each having a gate electrode and impurity regions forming source/drain as well as one capacitor; and bit and word lines for controlling an operation of the memory cells, a cell plate forming an
10 electrode of the capacitor being formed of the same layer as the gate electrode.